	I A II at a Na	T A	
	Application No.	Applicant(s)	
Notice of Allowability	10/779,650	TOGO, KIYOTAKE	
House of Anomability	Examiner	Art Unit	
	Paul Dinh	2825	······································
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to the communications from 2/18/04 to 8/10/05.			
2. The allowed claim(s) is/are <u>1-8</u> .			
3.			
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date 2/18/04 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	5. Notice of Informal Pa 6. Interview Summary (Paper No./Mail Date 8), 7. Examiner's Amendm 8. Examiner's Statemen 9. Other	(PTO-413), e nent/Comment	ŕ

Reasons for Allowance

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The following is an examiner's statement of reasons for allowance:

Claims 1-8 are allowed because the prior art of record does not teach or

Suggest a semiconductor integrated circuit device with a combinations of elements as recited in the claims; the device, among other things, having: a second memory controller that is provided outside the hard macro and controls the input/output from and to the external memory; an IO pad unit provided outside the hard macro as an electrical interface to the outside; a first wiring that connects the first memory controller and the IO pad unit; and a second wiring that connects the second memory controller and the IO pad unit and has a length shorter than the length of the first wiring.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul Dinh whose telephone number is 571-272-1890.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on 571-272-7483. The fax number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Paul Dinh

Paul Dinh

Primary Examiner